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[Creel] has a top ten that should appeal to many Hackaday readers: the top 10 craziest x86 assembly language instructions. You have to admit that the percentage of assembly language programmers is ...

Oddball X86 Instructions

Adding a custom instruction usually requires some manual effort, too. Typically ... In other words, you can have one app, and you can compile it for x86 or RISC-V. If the instruction is too complex, ...

Creating Domain Specific Processors Using Custom RISC-V ISA Instructions

In comparison, when we look at Microchip's 8051 range of MCUs, we can see in the [Atmel 8051 Microcontrollers Hardware Manual](#) in section ... 3 to 8 cycles. On x86 platforms the story ...

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Real-Time OS Basics: Picking The Right RTOS When You Need One

The fact that QNAP is based in Taiwan, where English is not the primary language, doesn't help its cause, either, but it is a large, successful international company. It needs to act that way. And I ...

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