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Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z
~~Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is~~
~~Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates~~

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~~and an inverter.~~

~~Consider the Q.~~

4.8: Design a code converter that converts a decimal

digit from the 8, 4, $\bar{2}$, $\bar{1}$ code to BCD Q.

5.4: A PN flip-flop has four operations: clear to 0, no change, complement, and set to 1, when Q. 5.8:

Derive the state table and the state diagram

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of the sequential
circuit shown in Fig.
P5.8

Q. 4.26: Construct a
4-to-16-line decoder
with five 2-to-4-line
decoders with enable.

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Unit 04 -

Combinational Logic -
Part 05 - Binary

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Mano index Q. 3.9:

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Find all the prime 5th
implicants for the
following Boolean
functions, and

determine which are

Q. 4.34: Implement
the following Boolean
function with a

multiplexer (HDL—see

Problem 4.48) What

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table/circuit diagram

(using D-flip flop) -

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Lecture 3b: Intro. to

the Labs \u0026

FPGAs (ETH Zürich,

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Implementation of

5x32 decoder using

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3x8 and 2x4 decoder

~~Combinational circuit
with decoder and
external logic gates~~

~~Digital Electronics~~

~~Example Q. 3.11:~~

Simplify the following
Boolean functions:
using five variable
maps: (a)

$F(A,B,C,D,E) = \text{Sum}$

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edition by Mano
& Ciletti Q. 1.1:
List the octal and
hexadecimal numbers
from 16 to 32. Using
A and B for the last
two digits Q. 7.19:
Tabulate the PLA
programming table for
the four Boolean
functions listed below.
~~Q. 3.15: Simplify the
following Boolean~~

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function F , together
with the don't-care
conditions d , and

Digital Design: Q.

1.13: Do the following
conversion problems:

(a) Convert decimal
27.315 to binary

Design of Digital
Circuits Lecture 5:
Combinational Logic
(ETH Zürich, Spring
2018)

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$$.630 \ .26 \ .52 \ .04$$

$$\text{Coefficient } a^{-1} = 0 \ a^{-2}$$

$$= 1 \ a^{-3} = 0 \ a^{-4} = 1$$

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M. Morris Mano is an
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Angeles. His notable works include the Mano Machine, i.e. a theoretical computer that contains a central processing unit, random access memory, and an input-output bus. M. Morris Mano has authored numerous books in the area of digital circuits that are known for teaching

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